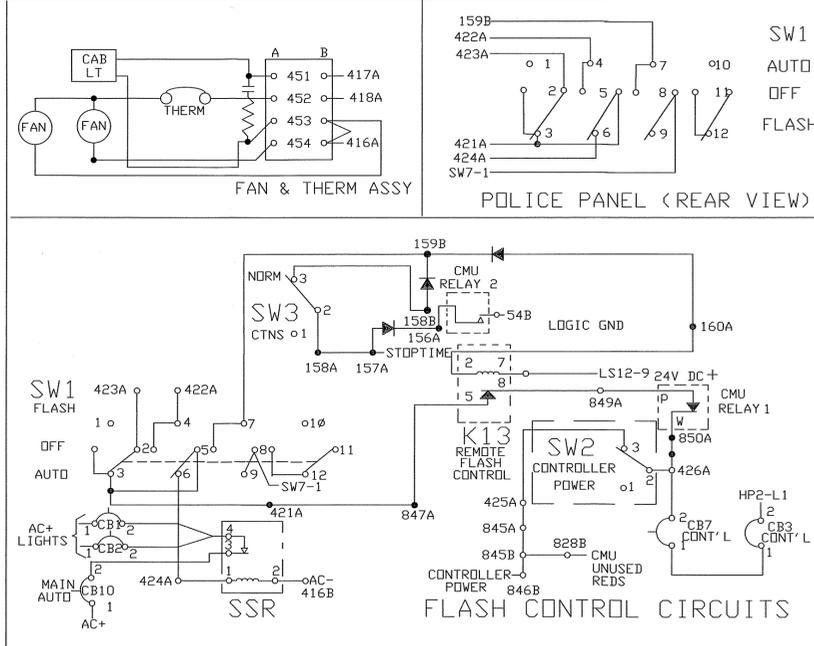
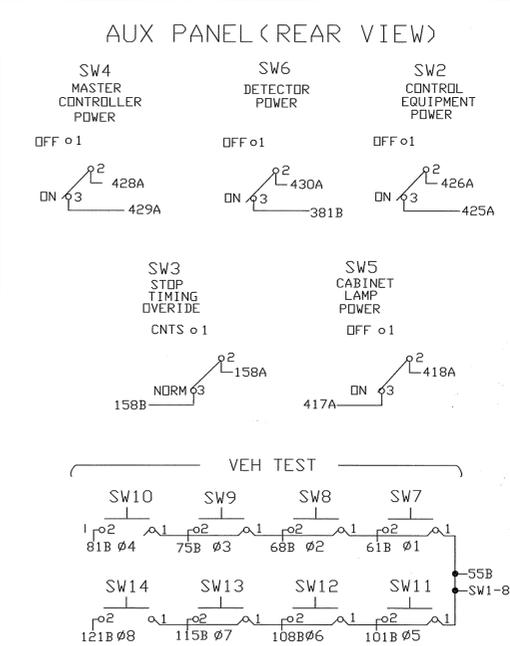
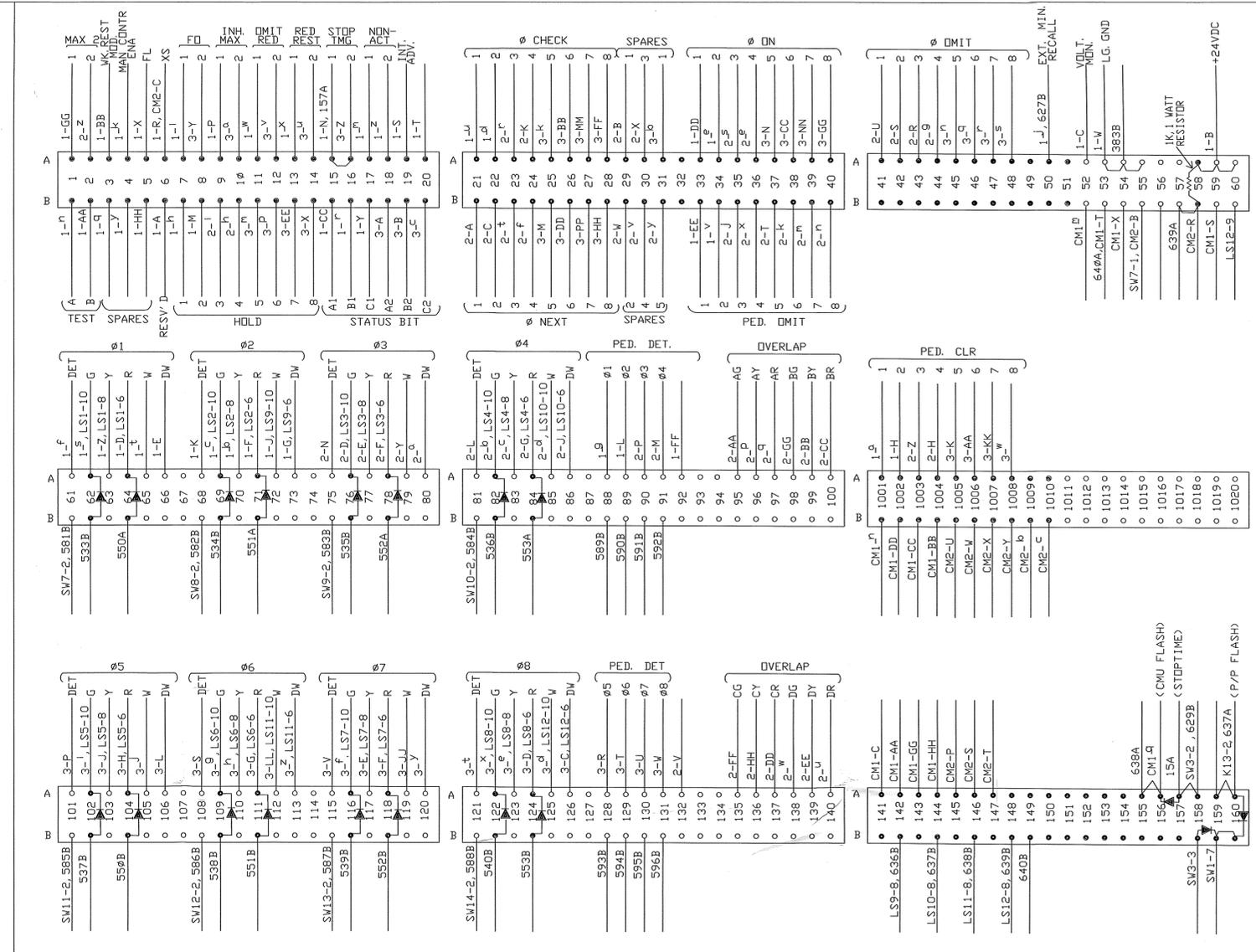


CONTROLLER INTERFACE PANEL

1	2	3
SHL SHELL GROUND 1-V	AL #1 PHASE NEXT 21B	AL STATUS BIT A2 18B
AL RESV. 6B	BL SPARE 1 29A	BL STATUS BIT B2 19B
BL 24VDC+ 59A	CL #2 PHASE NEXT 22B	CL #6 DWK 126A
CL VOLTAGE MONITOR 52A	DL #3 GRN 76A	DL #8 RED 124A
DL #1 RED 64A	EL #3 YEL 77A	EL #7 YEL 117A
EL #1 DWK 66A	FL #3 RED 78A	FL #7 RED 118A
FL #2 RED 71A	GL #4 RED 84A	GL #6 RED 111A
GL #2 DWK 73A	HL #4 PCL 1004A	HL #5 RED 104A
HL #2 PCL 1002A	JL #4 DWK 96A	JL #5 YEL 103A
JL #2 WK 72A	KL #4 CHECK 24A	KL #5 PCL 1005A
KL #2 VEH DET 68A	LL #4 VEH DET 81A	L #5 PHASE NEXT 106A
LL #2 PED DET 89A	ML #4 PED DET 91A	M #5 PHASE NEXT 25B
ML #2 HOLD 83A	NL #3 VEH DET 75A	N #5 PHASE DN 37A
NL STOP TIMING 1 15A	PL #3 PED DET 90A	PL #5 VEH DET 101A
PL INHIBIT MAX TERM 1 9A	RL #3 PHASE OMIT 43A	R #5 PED DET 128A
RI EXTERNAL START 6A	SL #2 PHASE OMIT 42A	S #6 VEH DET 128A
SI INTERVAL ADVANCE 19A	TL #5 PED OMIT 42A	T #6 PED DET 108A
T INDICATOR LAMP CNT 20A	UL #1 PHASE OMIT 37B	UL #7 PED DET 129A
UI AC- NEUTRAL NB1	VL PED RECYCLE 2 41A	V #7 VEH DET 130A
VI CHASSIS GROUND GB1	WL SPARE 2 132A	W #8 PED DET 115A
VII LOGIC GROUND 53A	XL SPARE 3 29B	X #8 HOLD 131A
VI FLASH LOGIC OUT 5A	YL #3 WK 30A	Y #8 FORCE OFF 2 14B
Y STATUS BIT C1 17B	ZL #3 PCL 79A	Z #8 STOP TIME 2 8A
Z #1 YEL 63A	aL #3 DWK 1003A	a #8 INHIBIT MAX TERM 2 16A
a #2 YEL 1001A	bL #4 GRN 80A	b #8 SPARE 1 10A
b #1 PCL 70A	cL #4 YEL 82A	c #8 STATUS BIT C2 31A
c #2 GRN 69A	dL #4 WALK 83A	d #8 WK 20B
d #2 CHECK 22A	eL #4 PHASE ON 85A	e #8 YEL 125A
e #2 PHASE DN 34A	fL #4 PHASE NEXT 36A	f #8 GRN 123A
f #1 VEH DET 61A	gL #4 PHASE OMIT 24B	g #7 GRN 116A
g #1 PED DET 88A	hL #4 HOLD 44A	h #6 GRN 109A
h #1 HOLD 7B	iL #3 HOLD 10B	i #6 YEL 110A
iL FORCE OFF 1 7A	jL #3 PED OMIT 9B	j #5 GRN 102A
JI EXT MIN RECALL ALL 50A	kL #6 PED OMIT 35B	k #5 WK 105A
KI MAN. CONTROL ENABLE 4A	lL #6 PED OMIT 38B	l #5 CHECK 25A
LI CALL TO NON-ACT I 17A	mL #7 PED OMIT 39B	m #5 HOLD 11B
MI TEST INPUT A 1B	nL #8 PED OMIT 40B	n #5 PHASE OMIT 45A
PI AC+ CONTROL 846B	PL #1 A YEL 96A	PL #6 HOLD 12B
QI SPARE 1 3B	RL #1 A RED 97A	QL #6 PHASE OMIT 46A
RI STATUS BIT B1 16B	RL #3 CHECK 23A	RL #7 PHASE OMIT 47A
SI #1 GRN 62A	SL #3 PHASE DN 35A	SL #8 PHASE OMIT 48A
TI #1 WK 65A	TL #3 PHASE NEXT 23B	TL #8 VEH DET 121A
UI #1 CHECK 21A	UL #1 D RED 140A	UL #8 PHASE OMIT 14A
V #2 PED OMIT 34B	WL #1 D GRN 30B	W #8 PCL 12A
VI DMIT RED CLR 11A	XL #4 PED OMIT 138A	X #8 GRN 1008A
VI SPARE 2 13A	YL SPARE 5 36B	Y #7 DWK 122A
Z #1 CALL TO NON-ACT II 18A	ZL MAX 2 SELECT 2 31B	Z #6 DWK 120A
AA TEST INPUT B 2B	AA #1 A GRN 2A	AA #6 PCL 113A
BB WALK REST MODIFIER 3A	BB #1 B YEL 95A	BB #6 CHECK 1006A
CC STATUS BIT A1 15B	BB #1 B RED 99A	BB #6 PHASE DN 26A
DD #1 PHASE DN 33A	CC #1 C RED 100A	CC #6 PHASE NEXT 38A
EE #1 PED OMIT 33B	DD #1 D YEL 137A	DD #7 HOLD 26B
FF PED RECYCLE 1 92A	EE #1 D GRN 139A	EE #7 CHECK 13B
GG MAX 2 SELECT 1A	FF #1 C GRN 135A	FF #8 CHECK 28A
HH SPARE 3 5B	GG #1 B GRN 98A	GG #8 PHASE DN 40A
	HH #1 C YEL 136A	HH #8 PHASE NEXT 28B
		JJ #7 WK 119A
		KK #7 PCL 1007A
		LL #6 WK 112A
		MM #7 CHECK 27A
		NN #7 PHASE DN 39A
		PP #7 PHASE NEXT 27B



#23 AT MARKETPLACE DR

REV	DATE	REVISION
1	10/31/01	
2		
3		
4		
5		
6		
7		
8		
9		
10		
11		
12		
13		
14		
15		
16		
17		
18		
19		
20		
21		
22		
23		
24		
25		
26		
27		
28		
29		
30		
31		
32		
33		
34		
35		
36		
37		
38		
39		
40		
41		
42		
43		
44		
45		
46		
47		
48		
49		
50		
51		
52		
53		
54		
55		
56		
57		
58		
59		
60		

REV. STATUS

SHEET 1 2 3

REV

TITLE: MNDOT 2002 "R" & "P" CABINET

SCALE: FILE: ACTORPPE

ACT Electronics, Inc.

