

**CSAH 1 @ South St. - SIGNAL CODE 7023 - BY PHASE TIMING  
PROPOSED - 01/03/2022**

**BY PHASE TIMING DATA**

PHASE	ø1	ø2	ø3	ø4	ø5	ø6	ø7	ø8
DIRECTION		NB		EB		SB		WB
MINIMUM GREEN		10		8		10		8
BK MIN GREEN								
CS MIN GREEN								
DELAY GREEN								
WALK		7		7		7		7
WALK 2				12				12
PED CLEAR		17		19		19		19
PED CLEAR 2				19				19
VEHICLE EXT		5.0		2.1		5.3		2.5
VEHICLE EXT 2								
MAX 1		40		25		40		25
MAX 2								
MAX 3								
DYM MAX								
DYM STP								
YELLOW		3.7		3.3		3.4		3.3
RED CLEAR		2.2		2.4		2.5		2.4
RED MAX								
RED REVERT		6.0		6.0		6.0		6.0
ACT B4								
SEC/ACT		1.5				1.5		
MAX INT		23				23		
TIME B4		14				14		
CARS WT								
STPT DUC								
TIME TO REDUCE		14				14		
MIN GAP		2.5				2.0		

**Detector Extend/Delay**

D4-2 delay - 2.0 sec.

D4-3 delay - 5.0 sec.

D8-1 extend - 1.0 sec.

D8-2 delay - 5.0 sec.

D8-3 delay - 2.0 sec.

D2-2 & D2-3 detectors were broken due to construction activities. D6-2 & D6-3 detectors will be disabled so that phases 2 and 6 will have similar operation.

Logic will enable Ped Walk 2 + Ped Clearance 2 if push button pressed for 3 seconds.  
Intended to enhance safety for crossing guard during AM arrival and PM dismissal periods for Anoka Middle School and Lincoln Elementary

# Logic Statements to Call Pedestrian Walk and Clearance #2 CSAH 1 (5<sup>th</sup> St.) @ South St.

p501-UI PANEL-12.64.00 ASC/3-2100 - C:\ASC3\...

LP#: 1 COPY FROM: 1 ACTIVE:M FALSE  
IF DET PED 4 IS ON F

THEN LP DELAY FOR 3.0 SECONDS  
LP SET LOGIC FLAG 1 ON

ELSE LP SET LOGIC FLAG 1 OFF

R1 02 04 I  
R2 06 08 I  
R3  
R4

MM SM ND NS  
U  
L E R  
D  
H Status NP Small

1	2	3
4	5	6
7	8	9
SF	0	C

p501-UI PANEL-12.64.00 ASC/3-2100 - C:\ASC3\...

LP#: 2 COPY FROM: 2 ACTIVE:M FALSE  
IF LP FLAG 1 IS ON F

THEN DET CALL PED2 PH 4 ON  
LP SET LOGIC FLAG 1 OFF

ELSE

R1 02 04 I  
R2 06 08 I  
R3  
R4

MM SM ND NS  
U  
L E R  
D  
H Status NP Small

1	2	3
4	5	6
7	8	9
SF	0	C

p501-UI PANEL-12.64.00 ASC/3-2100 - C:\ASC3\...

LP#: 3 COPY FROM: 3 ACTIVE:M FALSE  
IF DET PED 8 IS ON F

THEN LP DELAY FOR 3.0 SECONDS  
LP SET LOGIC FLAG 2 ON

ELSE LP SET LOGIC FLAG 2 OFF

R1 02 04 I  
R2 06 08 I  
R3  
R4

MM SM ND NS  
U  
L E R  
D  
H Status NP Small

1	2	3
4	5	6
7	8	9
SF	0	C

p501-UI PANEL-12.64.00 ASC/3-2100 - C:\ASC3\...

LP#: 4 COPY FROM: 4 ACTIVE:M FALSE  
IF LP FLAG 2 IS ON F

THEN DET CALL PED2 PH 8 ON  
LP SET LOGIC FLAG 2 OFF

ELSE

R1 02 04 I  
R2 06 08 I  
R3  
R4

MM SM ND NS  
U  
L E R  
D  
H Status NP Small

1	2	3
4	5	6
7	8	9
SF	0	C

Note:  
Person must hold Phase 4 or Phase 8 ped button(s) for 3 seconds to initiate logic statements