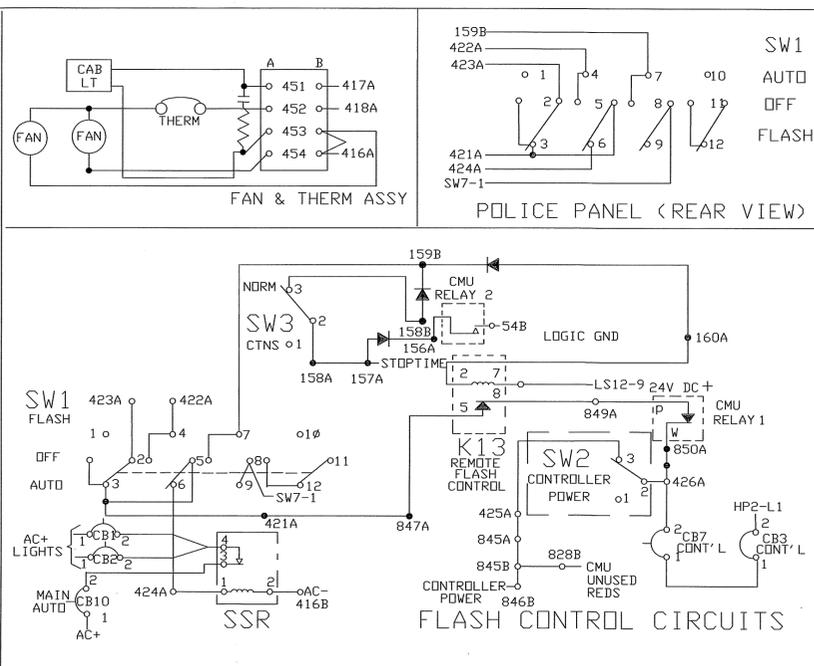
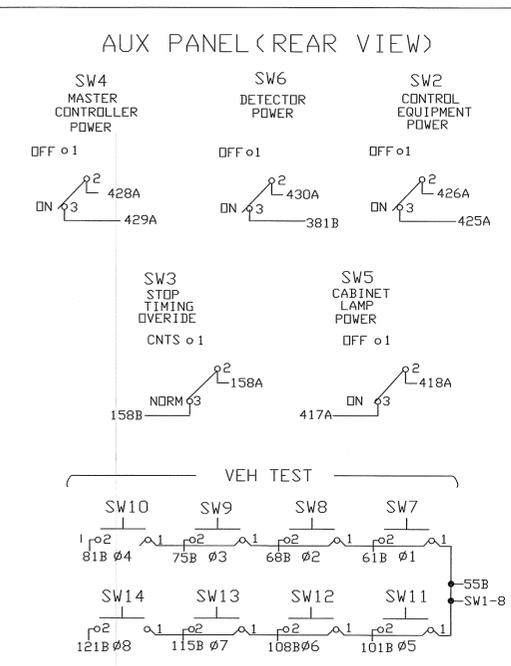
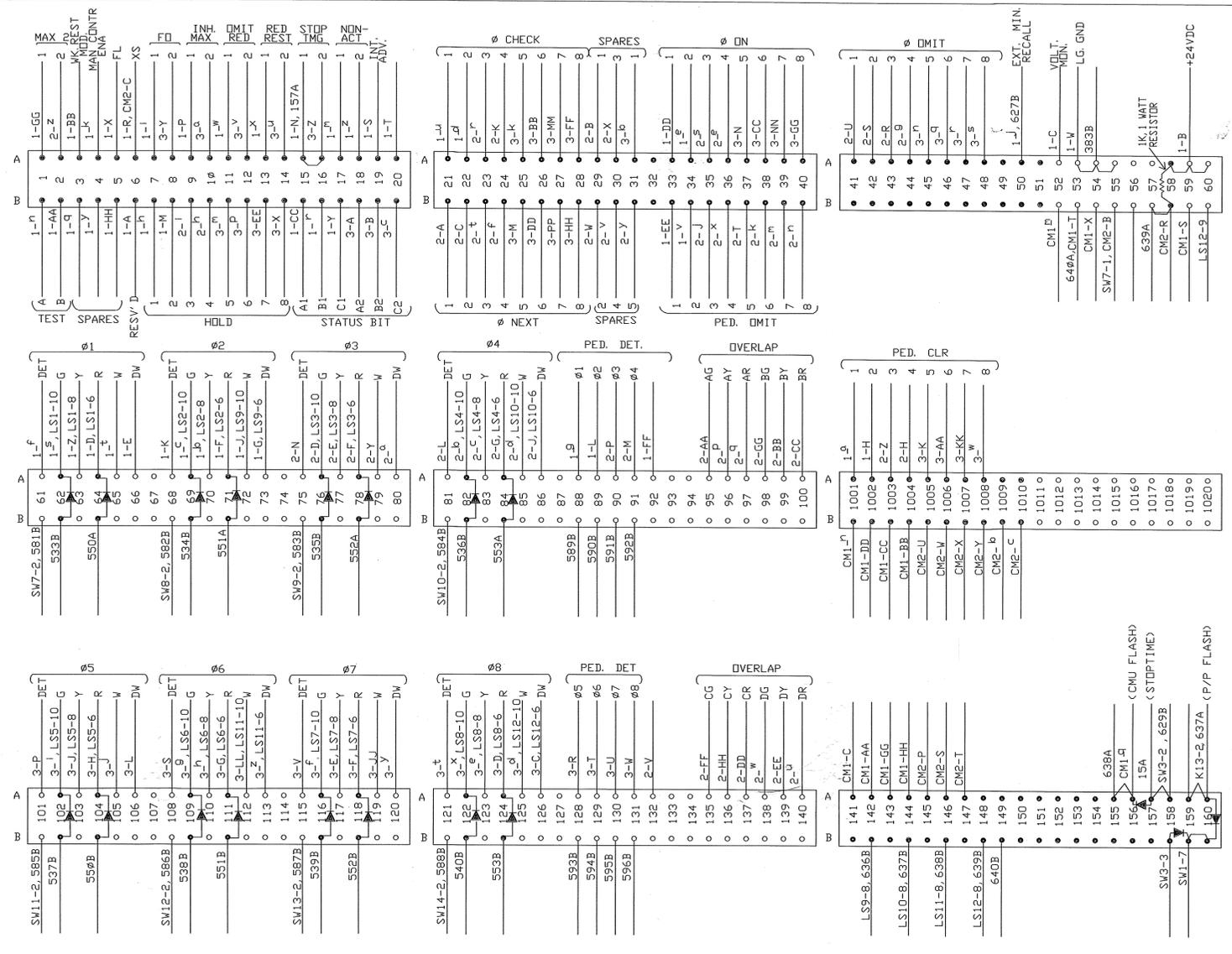


CONTROLLER INTERFACE PANEL

1	2	3
SHL SHELL GROUND 1-V	AL #1 PHASE NEXT 21B	AL STATUS BIT A2 18B
AL RESV. 6B	BL SPARE 1 29A	BL STATUS BIT B2 19B
BL 24VDC+ 59A	CL #2 PHASE NEXT 22B	CL #8 DWK 126A
CL VOLTAGE MONITOR 52A	DL #3 GRN 76A	DL #7 YEL 124A
DL #1 RED 64A	EL #3 YEL 77A	EL #7 RED 117A
EL #1 DWK 66A	FL #3 RED 78A	FL #7 RED 118A
FL #2 RED 71A	GL #4 RED 84A	GL #5 RED 111A
GL #2 DWK 73A	HL #4 PCL 100A	HL #5 RED 104A
HL #2 PCL 1002A	JL #4 DWK 86A	JL #5 YEL 103A
JL #2 WK 72A	KL #4 CHECK 24A	KL #5 PCL 1005A
KL #2 VEH DET 68A	LL #4 VEH DET 81A	LL #5 DWK 106A
LL #2 PED DET 89A	ML #4 PED DET 91A	ML #5 PHASE NEXT 25B
ML #2 HOLD 8B	NL #3 VEH DET 75A	NL #5 PHASE ON 37A
NL STOP TIMING 1 15A	PL #3 PED DET 90A	PL #5 VEH DET 101A
PL INHIBIT MAX TERM 1 9A	RL #3 PHASE OMIT 43A	RL #5 PED DET 128A
RL EXTERNAL START 6A	SL #2 PHASE OMIT 42A	SL #6 VEH DET 108A
SL INTERVAL ADVANCE 19A	TL #5 PED OMIT 37B	TL #6 PED DET 129A
TL INDICATOR LAMP CONT 20A	UL #1 PHASE OMIT 41A	UL #7 PED DET 130A
UL AC- NEUTRAL NB1	VL PED RECYCLE 2 132A	VL #7 VEH DET 115A
VL CHASSIS GROUND GB1	WL SPARE 2 29B	WL #8 PED DET 131A
WL LOGIC GROUND 53A	XL SPARE 3 30A	XL #8 HOLD 14B
XL FLASH LOGIC OUT 5A	YL #3 WK 79A	YL FORCE OFF 2 8A
YL STATUS BIT C1 17B	ZL #3 PCL 1003A	ZL STOP TIME 2 16A
ZL #1 YEL 63A	aL #3 DWK 80A	aL INHIBIT MAX TERM 2 10A
aL #2 YEL 70A	bL #4 GRN 82A	bL SPARE 1 31A
bL #2 GRN 69A	cL #4 YEL 83A	cL STATUS BIT C2 20B
cL #2 CHECK 22A	dL #4 WALK 85A	dL #8 WK 125A
dL #2 PHASE ON 34A	eL #4 PHASE ON 36A	eL #8 YEL 123A
eL #1 VEH DET 61A	fL #4 PHASE NEXT 24B	fL #7 GRN 116A
fL #1 PED DET 88A	gL #4 PHASE OMIT 44A	gL #6 GRN 109A
gL #1 HOLD 7B	hL #4 HOLD 10B	hL #6 YEL 110A
hL #1 HOLD 7A	iL #3 HOLD 9B	iL #5 GRN 102A
iL FORCE OFF 1 7A	jL #3 PED OMIT 35B	jL #5 WK 105A
jL EXT MIN RECALL ALL 50A	kL #6 PED OMIT 38B	kL #5 CHECK 25A
kL MAN. CONTROL ENABLE 4A	mL #7 PED OMIT 39B	mL #5 HOLD 11B
mL CALL TO NON-ACT I 17A	nL #8 PED OMIT 40B	nL #5 PHASE OMIT 45A
nL TEST INPUT A 1B	pL AC+ CONTROL 846B	pL #6 HOLD 12B
pL AC+ CONTROL 846B	qL SPARE 1 3B	qL #6 PHASE OMIT 46A
qL SPARE 1 3B	rL STATUS BIT B1 16B	rL #7 PHASE OMIT 47A
rL STATUS BIT B1 16B	sL #1 GRN 62A	sL #8 PHASE OMIT 48A
sL #1 GRN 62A	tL #1 WK 65A	tL #8 VEH DET 121A
tL #1 WK 65A	uL #1 CHECK 21A	uL RED REST MODE 2 14A
uL #1 CHECK 21A	vL #2 CHECK OMIT 34B	vL OMIT RED CLR 2 12A
vL #2 CHECK OMIT 34B	wL OMIT RED CLR 11A	wL #8 PCL 1008A
wL OMIT RED CLR 11A	xL RED REST MODE 1 13A	xL #8 GRN 122A
xL RED REST MODE 1 13A	yL SPARE 2 4B	yL #7 DWK 120A
yL SPARE 2 4B	zL CALL TO NON-ACT II 18A	zL #6 DWK 113A
zL CALL TO NON-ACT II 18A	AA TEST INPUT B 2B	AA #6 PCL 1006A
AA TEST INPUT B 2B	BB WALK REST MODIFIER 3A	BB #6 CHECK 26A
BB WALK REST MODIFIER 3A	CC STATUS BIT A1 15B	CC #6 PHASE ON 38A
CC STATUS BIT A1 15B	DD #1 PHASE ON 100A	DD #6 PHASE NEXT 38A
DD #1 PHASE ON 100A	EE #1 PED OMIT 33B	EE #7 HOLD 26B
EE #1 PED OMIT 33B	FF PED RECYCLE 1 92A	FF #9 CHECK 13B
FF PED RECYCLE 1 92A	GG MAX 2 SELECT 1A	GG #8 PHASE ON 28A
GG MAX 2 SELECT 1A	HH SPARE 3 5B	GG DL B GRN 40A
HH SPARE 3 5B		HH #8 PHASE NEXT 28B
		JJL #7 WK 119A
		KKL #7 PCL 1007A
		LLL #6 WK 112A
		MM #7 CHECK 27A
		NN #7 PHASE ON 39A
		PP #7 PHASE NEXT 27B



NORTHDALE AT VILLAGE 10 CENTER

REV	DATE	REVISION
DATE	10/26/01	
DRAWN	NPD	
<p>ACT Electronics, Inc.</p>		
<p>TITLE: MNDOT 2002 "R" & "P" CABINET</p>		
<p>SHEET: 1 2 3</p>		
<p>REV: 1 2 3</p>		
<p>SCALE: _____</p>		
<p>FILE: ACT02R2P SHEET 2 OF 3</p>		

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