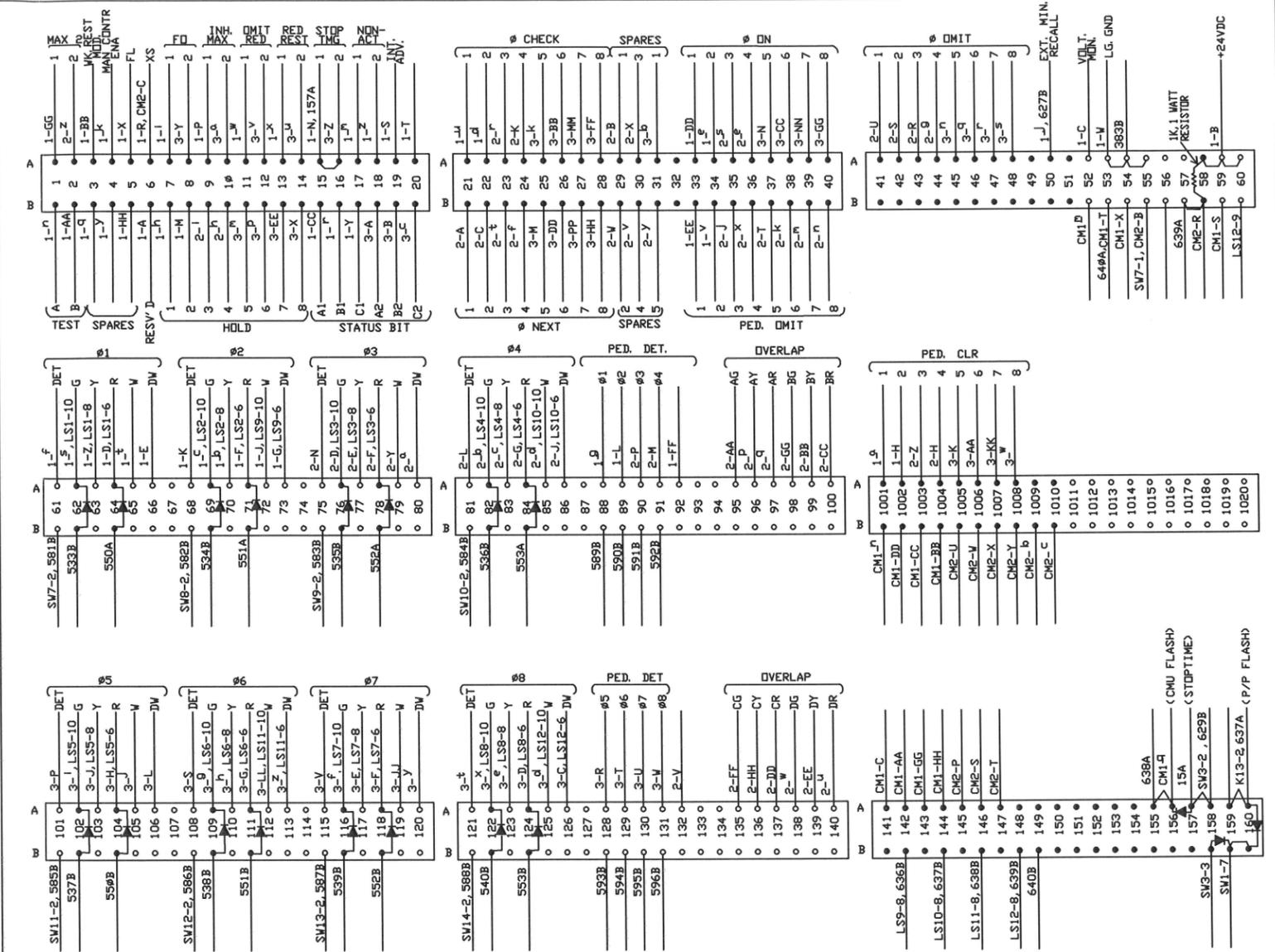
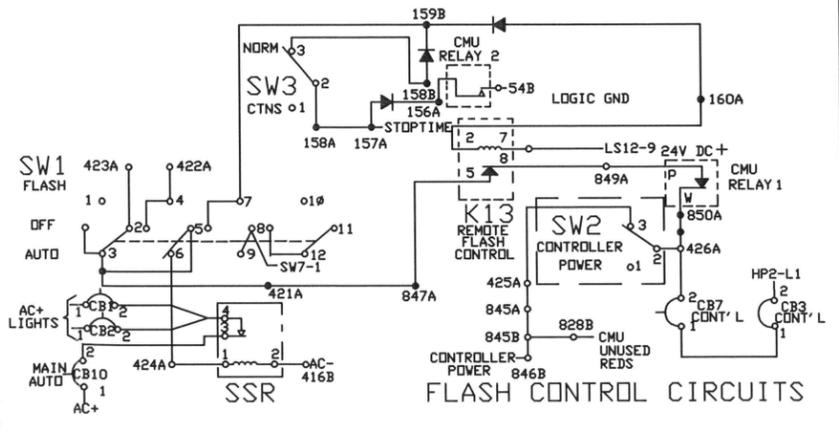
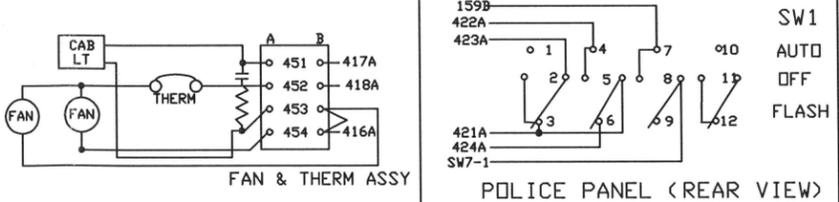
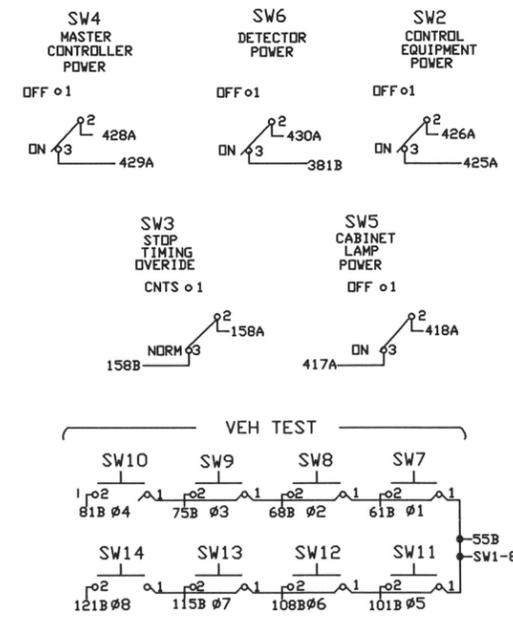


CONTROLLER INTERFACE PANEL

1	2	3
SHL SHELL GROUND 1-V AL RESV. 6B BL 24VDC+ 59A CL VOLTAGE MONITOR 52A DL #1 RED 64A EL #1 DWK 66A FL #2 RED 71A GL #2 DWK 73A HL #2 PCL 1002A JL #2 WK 72A KL #2 VEH DET 68A LL #2 PED DET 89A ML #2 HOLD 8B NL STOP TIMING 1 15A PL INHIBIT MAX TERM 1 9A RL EXTERNAL START 6A SL INTERVAL ADVANCE 19A TL INDICATOR LAMP CONT 20A UL AC- NEUTRAL NB1 VL CHASSIS GROUND GB1 WL LOGIC GROUND 53A XL FLASH LOGIC OUT 5A YL STATUS BIT C1 17B ZL #1 YEL 63A aL #1 PCL 1001A bL #2 YEL 70A cL #2 GRN 69A dL #2 CHECK 22A eL #2 PHASE DN 34A fL #1 VEH DET 61A gL #1 PED DET 88A hL #1 HOLD 7B iL FORCE OFF 1 7A JL EXT MIN RECALL ALL 50A kL MAN. CONTROL ENABLE 4A mL CALL TO NON-ACT I 17A nL TEST INPUT A 1B pL AC+ CONTROL 846B qL SPARE 1 3B rL STATUS BIT B1 16B sL #1 GRN 62A tL #1 WK 65A uL #1 CHECK 21A vL #2 PED OMIT 34B wL OMIT RED CLR 11A xL RED REST MODE 1 13A yL SPARE 2 4B zL CALL TO NON-ACT II 18A AAL TEST INPUT B 2B BBL WALK REST MODIFIER 3A CCL STATUS BIT A1 15B DDL #1 PHASE DN 33A EEL #1 PED OMIT 33B FFL PED RECYCLE 1 92A GGL MAX 2 SELECT 1A HHL SPARE 3 5B	AL #1 PHASE NEXT 21B BL SPARE 1 29A CL #2 PHASE NEXT 22B DL #3 GRN 76A EL #3 YEL 77A FL #3 RED 78A GL #4 RED 84A HL #4 PCL 1004A JL #4 DWK 86A KL #4 CHECK 24A LL #4 VEH DET 81A ML #4 PED DET 91A NL #3 VEH DET 75A PL #3 PED DET 90A RL #3 PHASE OMIT 43A SL #2 PHASE OMIT 42A TL #5 PED OMIT 37B UL #1 PHASE OMIT 41A VL PED RECYCLE 2 132A WL SPARE 2 29B XL SPARE 3 30A YL #3 WK 79A ZL #3 PCL 1003A aL #3 DWK 80A bL #4 GRN 82A cL #4 YEL 83A dL #4 WALK 85A eL #4 PHASE DN 36A fL #4 PHASE NEXT 24B gL #4 PHASE OMIT 44A hL #4 HOLD 10B iL #3 HOLD 9B JL #3 PED OMIT 35B kL #6 PED OMIT 38B mL #7 PED OMIT 39B nL #8 PED OMIT 40B pL DL A YEL 96A qL DL A RED 97A rL #3 CHECK 23A sL #3 PHASE DN 35A tL #3 PHASE NEXT 23B uL DL D RED 140A vL SPARE 4 30B wL DL D GRN 138A xL #4 PED OMIT 36B yL SPARE 5 31B zL MAX 2 SELECT 2 2A AAL DL A GRN 95A BBL DL B YEL 99A CCL DL B RED 100A DDL DL C RED 137A EEL DL D YEL 139A FFL DL C GRN 135A GGL DL B GRN 98A HHL DL C YEL 136A	AL STATUS BIT A2 18B BL STATUS BIT B2 19B CL #8 DWK 126A DL #8 RED 124A EL #7 YEL 117A FL #7 RED 118A GL #6 RED 111A HL #5 RED 104A JL #5 YEL 103A KL #5 PCL 1005A LL #5 DWK 106A ML #5 PHASE NEXT 25B NL #5 PHASE DN 37A PL #5 VEH DET 101A RL #5 PED DET 128A SL #6 VEH DET 108A TL #6 PED DET 129A UL #7 PED DET 130A VL #8 PED DET 131A XL #8 HOLD 14B YL FORCE OFF 2 8A ZL STOP TIME 2 16A aL INHIBIT MAX TERM 2 10A bL SPARE 1 31A cL STATUS BIT C2 20B dL #8 WK 125A eL #8 YEL 123A fL #7 GRN 116A gL #6 GRN 109A hL #6 YEL 110A iL #5 GRN 102A JL #5 WK 105A kL #5 CHECK 25A mL #5 HOLD 11B nL #5 PHASE OMIT 45A pL #6 HOLD 12B qL #6 PHASE OMIT 46A rL #7 PHASE OMIT 47A sL #8 PHASE OMIT 48A tL #8 VEH DET 121A uL RED REST MODE 2 14A vL OMIT RED CLR 2 12A wL #8 PCL 1008A xL #8 GRN 122A yL #7 DWK 120A zL #6 DWK 113A AAL #6 PCL 1006A BBL #6 CHECK 26A CCL #6 PHASE DN 38A DDL #6 PHASE NEXT 26B EEL #7 HOLD 13B FFL #8 CHECK 28A GGL #8 PHASE DN 40A HHL #8 PHASE NEXT 28B JLL #7 WK 119A KKL #7 PCL 1007A LLL #6 WK 112A MML #7 CHECK 27A NNL #7 PHASE DN 39A PPL #7 PHASE NEXT 27B



AUX PANEL (REAR VIEW)



REV. STATUS		#14 AT OTTER LAKE	
SHEET 1	2	3	
REV			
TITLE MNDOT 2002 'R' & 'P' CABINET		REVISION DATE 10/26/01 DRAWN HPO	
SCALE D		ACT Electronics, Inc.	
FILE ACT2002R		SHEET 8 OF 3	

