

VEHICLE SIGNALS						
SIGNAL	TERMINAL					
	←-E	←-Y	←-R	G	Y	R
1-1	201	203	205			
1-2	202	204	206			
2-1				207	209	211
2-2				208	210	212
2-3				207	209	211
4-1				219	221	223
4-2				220	222	224
4-3				219	221	223
5-1	225	227	229			
5-2	226	228	230			
6-1				231	233	235
6-2				232	234	236
6-3				231	233	235
8-1				243	245	247
8-2				244	246	248
8-3				243	245	247

SIGNAL HEAD NEUTRALS TO BE
TERMINATED ON FNB1.

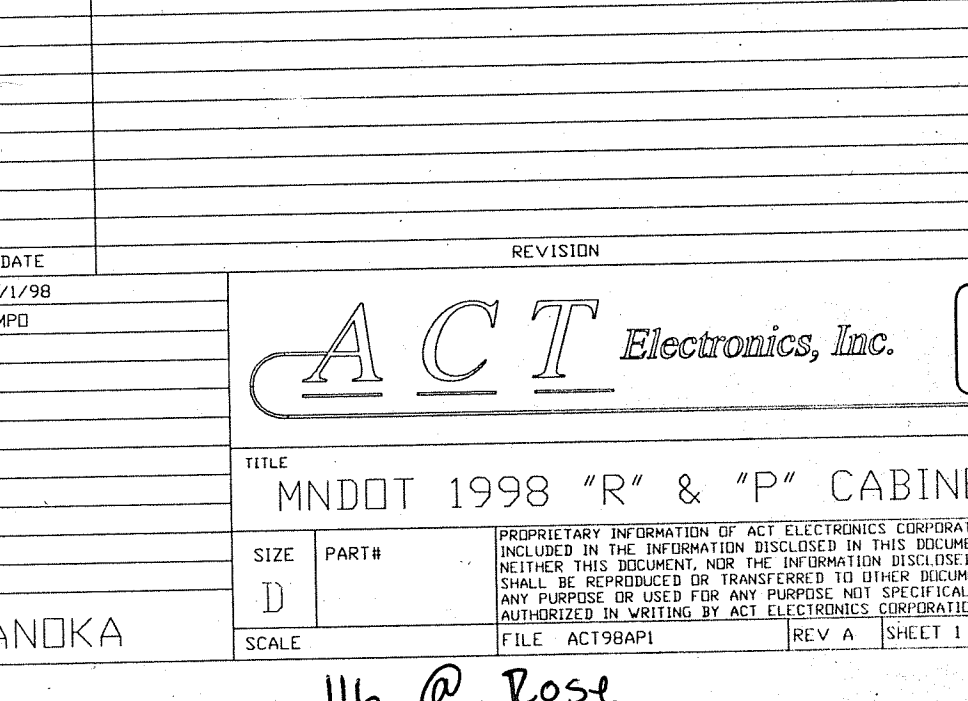
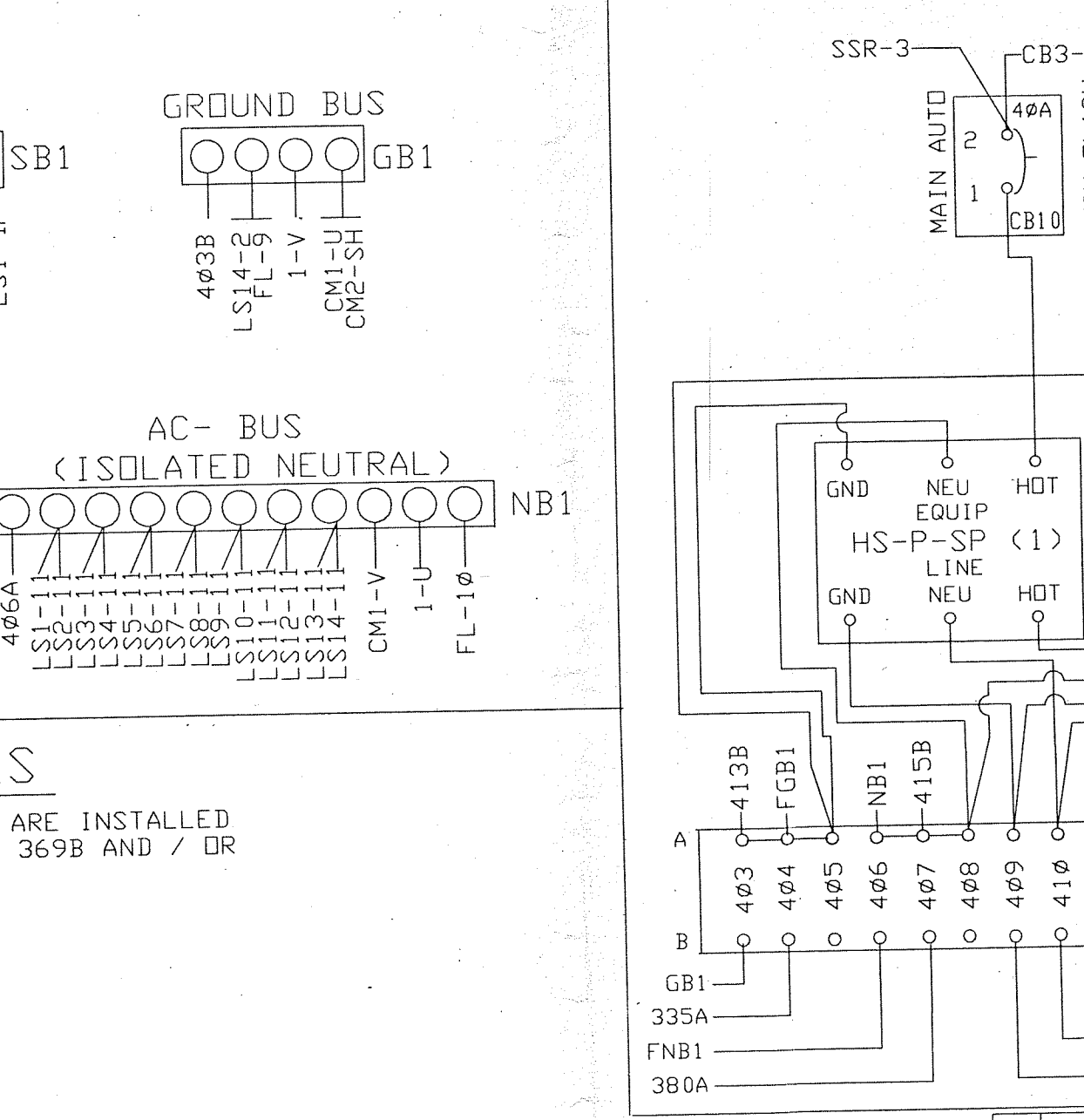
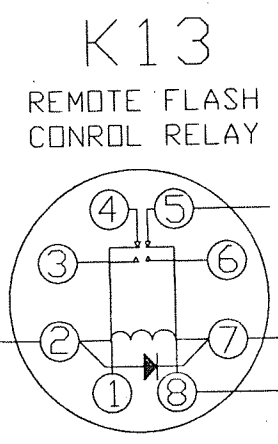
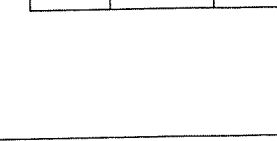
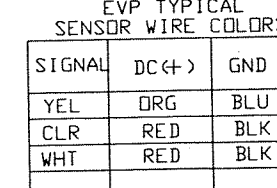
LOOP LEAD IN DRAIN WIRES ARE
TO BE CONNECTED TO GREEN/TAN
WIEDMULLER BLOCKS ONLY

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EVP TYPICAL SENSOR WIRE COLORS		
SIGNAL	DC (+)	GND
YEL	ORG	BLU
CLR	RED	BLK
WHT	RED	BLK

NOTES

1) IF EVP HEADS ARE
JUMPER 367B TO 369
335B TO 337B



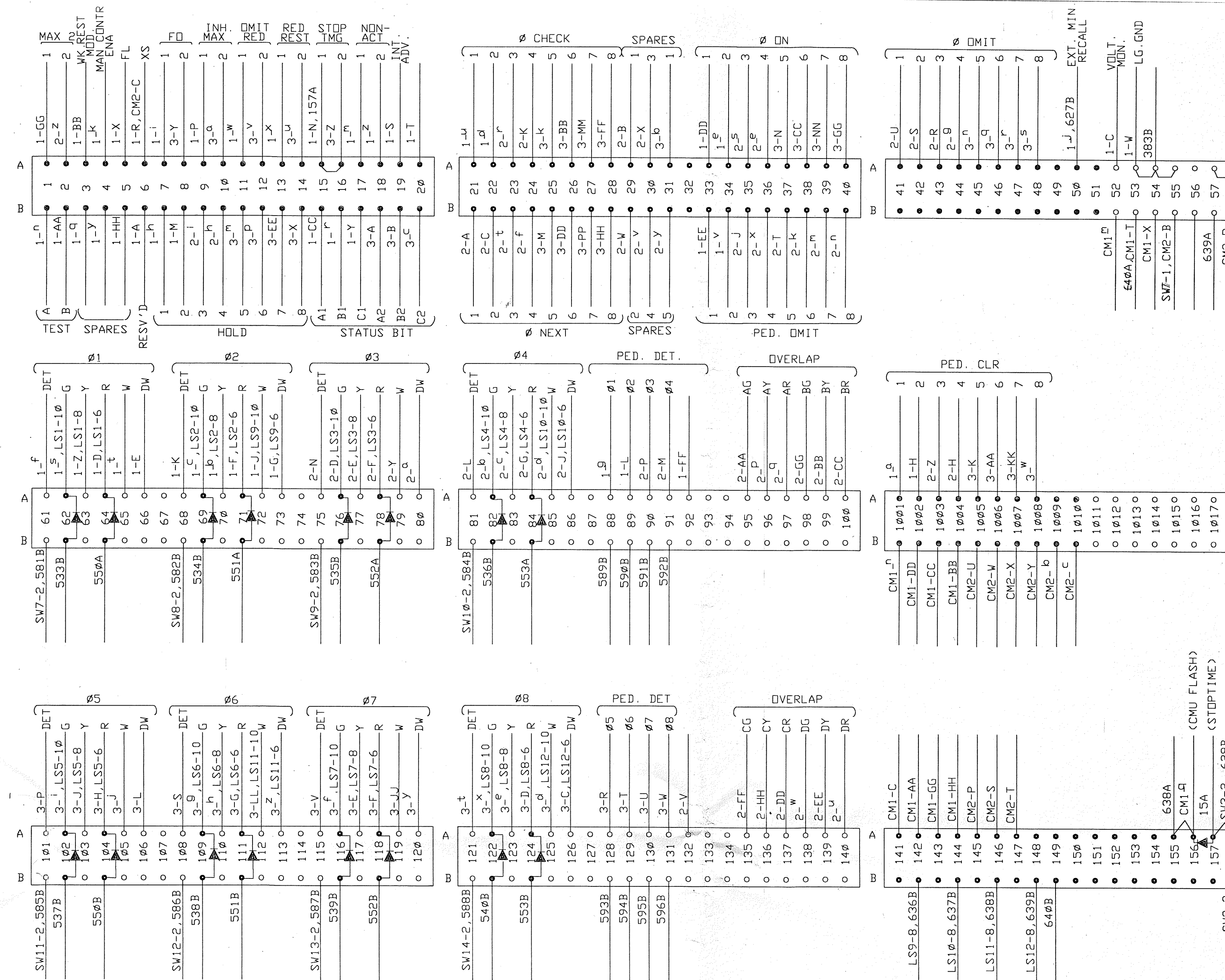
CSAH # 116/ BUNKER AT
ROSE ST/ DOWNTOWN CENTER
PROJECT # 02-716-04
CODE # 7142
3-12-99 - E.J.H.

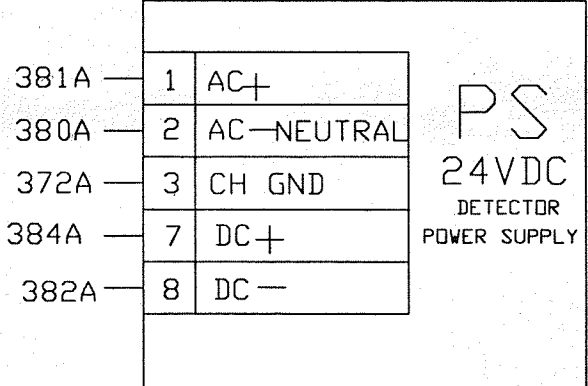
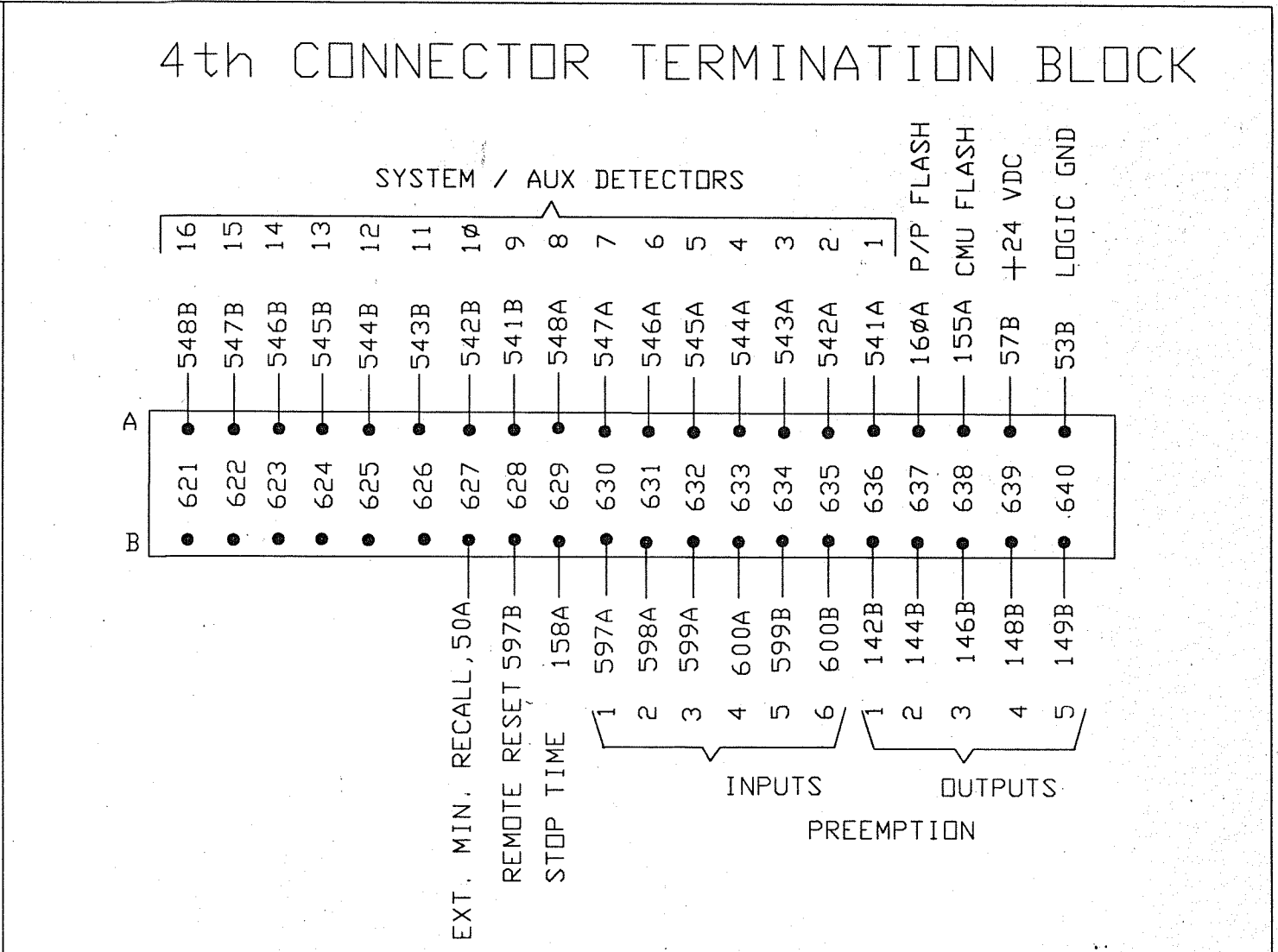
REV. STATUS				
SHEET	1	2	3	
REV	A	A	A	

REV	DATE	REVISION
DATE	12/1/90	
DRAWN	MPI	
		
TITLE		
MNDOT 1998 "R" & "P" CABINET		
SIZE PARTS		
D		
SCALE		
FUR: ANOKA		
PROSEQUEST INFORMATION OF ACT ELECTRONICS COMPANY INCLUDED IN THE INFORMATION PROVIDED IN THIS DOCUMENT THIS DOCUMENT AND THE INFORMATION THEREON SHALL BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, ELECTRONIC OR MECHANICAL, WITHOUT PERMISSION OF ACT ELECTRONICS COMPANY.		
FILE ACTRPM1		

CONTROLLER INTERFACE PANEL

1	2	3
SHL SHELL GROUND 1-V	A 01 PHASE NEXT 21B	A STATUS BIT A2 18B
AL RESV. 6B	B SPARE 1 29A	B STATUS BIT B2 19B
BL 24VDC+ 59A	C 02 PHASE NEXT 22B	C 08 DWK 126A
CL VOLTAGE MONITOR 52A	D 03 GRN 76A	D 08 RED 124A
DL 01 RED 64A	E 03 YEL 77A	E 07 YEL 117A
FL 02 RED 66A	F 04 RED 78A	F 06 RED 118A
GL 02 DWK 71A	G 04 RED 84A	G 05 RED 111A
HL 02 PCL 1002A	H 04 PCL 1004A	H 05 YEL 104A
JL 02 WK 72A	J 04 DWK 86A	J 05 PCL 103A
KL 02 VEH DET 68A	K 04 CHECK 24A	K 05 PCL 1005A
LL 02 PED DET 89A	L 04 VEH DET 81A	L 05 DWK 106A
ML 02 HOLD 9B	M 04 PED DET 91A	M 05 PHASE NEXT 25B
NL STOP TIMING 1 15A	N 04 VEH DET 75A	N 05 PHASE DN 37A
PL INHIBIT MAX TERM 1 9A	P 03 PED DET 90A	P 05 VEH DET 101A
RL EXTERNAL START 6A	R 03 PHASE OMIT 43A	R 05 PED DET 128A
SL INTERVAL ADVANCE 19A	S 02 PHASE OMIT 42A	S 06 VEH DET 108A
TL INDICATOR LAMP CONT 20A	T 05 PED OMIT 37B	T 06 PED DET 129A
UL AC- NEUTRAL NB1	U 01 PHASE OMIT 41A	U 07 PED DET 130A
V CHASSIS GROUND GB1	V PED RECYCLE 2 132A	V 07 VEH DET 115A
WL LOGIC GROUND 53A	W SPARE 2 29B	W 08 PED DET 131A
XL FLASH LOGIC OUT 5A	X SPARE 3 30A	X 08 HOLD 14B
YL STATUS BIT C1 17B	Y 03 WK 79A	Y FORCE OFF 2 8A
ZL 01 YEL 63A	Z 03 PCL 1003A	Z STOP TIME 2 16A
a 01 PCL 1001A	a 03 DWK 80A	a INHIBIT MAX TERM 2 10A
b 02 YEL 70A	b 04 GRN 82A	b SPARE 1 31A
c 02 GRN 69A	c 04 YEL 83A	c STATUS BIT C2 20B
d 02 CHECK 22A	d 04 WALK 85A	d 08 WK 125A
e 02 PHASE ON 34A	e 04 PHASE ON 85A	e 08 YEL 123A
f 01 VEH DET 61A	f 04 PHASE NEXT 24B	f 07 GRN 116A
g 01 PED DET 88A	g 04 PHASE OMIT 44A	g 06 GRN 109A
h 01 HOLD 7B	h 04 HOLD 10B	h 06 YEL 110A
i FORCE OFF 1 7A	i 03 HOLD 9B	i 05 GRN 102A
j EXT MIN RECALL ALL 50A	j 03 PED OMIT 35B	j 05 WK 105A
k MAN. CONTROL ENABLE 4A	k 06 PED OMIT 38B	k 05 CHECK 25A
m CALL TO NON-ACT I 17A	m 07 PED OMIT 39B	m 05 HOLD 11B
n TEST INPUT A 1B	n 08 PED OMIT 40B	n 05 PHASE OMIT 45A
p AC+ CONTROL 846B	p DL A YEL 96A	p 06 HOLD 12B
q SPARE 1 3B	q DL A RED 97A	q 06 PHASE OMIT 46A
r STATUS BIT B1 16B	r 03 CHECK 23A	r 07 PHASE OMIT 47A
s 01 GRN 62A	s 03 PHASE ON 35A	s 08 PHASE OMIT 48A
t 01 WK 65A	t 03 PHASE NEXT 23B	t 08 VEH DET 121A
u 01 CHECK 21A	u DL D RED 140A	u RED REST MODE 2 14A
v 02 PED OMIT 34B	v SPARE 4 30B	v OMIT RED CLR 2 12A
w OMIT RED CLR 11A	w DL D GRN 138A	w 08 PCL 1008A
x RED REST MODE 1 13A	x 04 PED OMIT 36B	x 08 GRN 122A
y SPARE 2 4B	y SPARE 5 31B	y 07 DWK 120A
z CALL TO NON-ACT II 10A	z MAX 2 SELECT 2 2A	z 06 DWK 113A
AA TEST INPUT B 2B	AA DL A GRN 95A	AA 06 PCL 1006A
BB WALK REST MODIFIER 3A	BB DL B YEL 99A	BB 06 CHECK 26A
CC STATUS BIT A1 15B	CC DL B RED 100A	CC 06 PHASE ON 38A
DD 01 PHASE ON 33A	DD DL C RED 137A	DD 06 PHASE NEXT 26B
EE 01 PED OMIT 33B	EE DL D YEL 139A	EE 07 HOLD 13B
FF PED RECYCLE 1 92A	FF DL C GRN 135A	FF 08 CHECK 28A
GG MAX 2 SELECT 1A	GG DL B GRN 98A	GG 08 PHASE ON 40A
HH SPARE 3 5B	HH DL C YEL 136A	HH 08 PHASE NEXT 28B
		JJ 07 WK 119A
		KK 07 PCL 1007A
		LL 06 WK 112A
		MM 07 CHECK 27A
		NN 07 PHASE ON 39A
		PP 07 PHASE NEXT 27B





MAIN T/O BLOCK		WIRE COLOR		SA I/O BLOCK		EDGE CONN		Pin 10 of the edge conn. can be jumpered for use as CH4 delay inhibit. Jumpering will disable addressing.		CH 1 CH 2 CH 3 CH 4 MODEL		SA1			SA2			SA3			SA4			SA5			SA6			SA7			SA8			FUNCTIONS																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																										
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REV. STATUS				
SHEET	1	2	3	
REV	A	A	A	

[illegible]