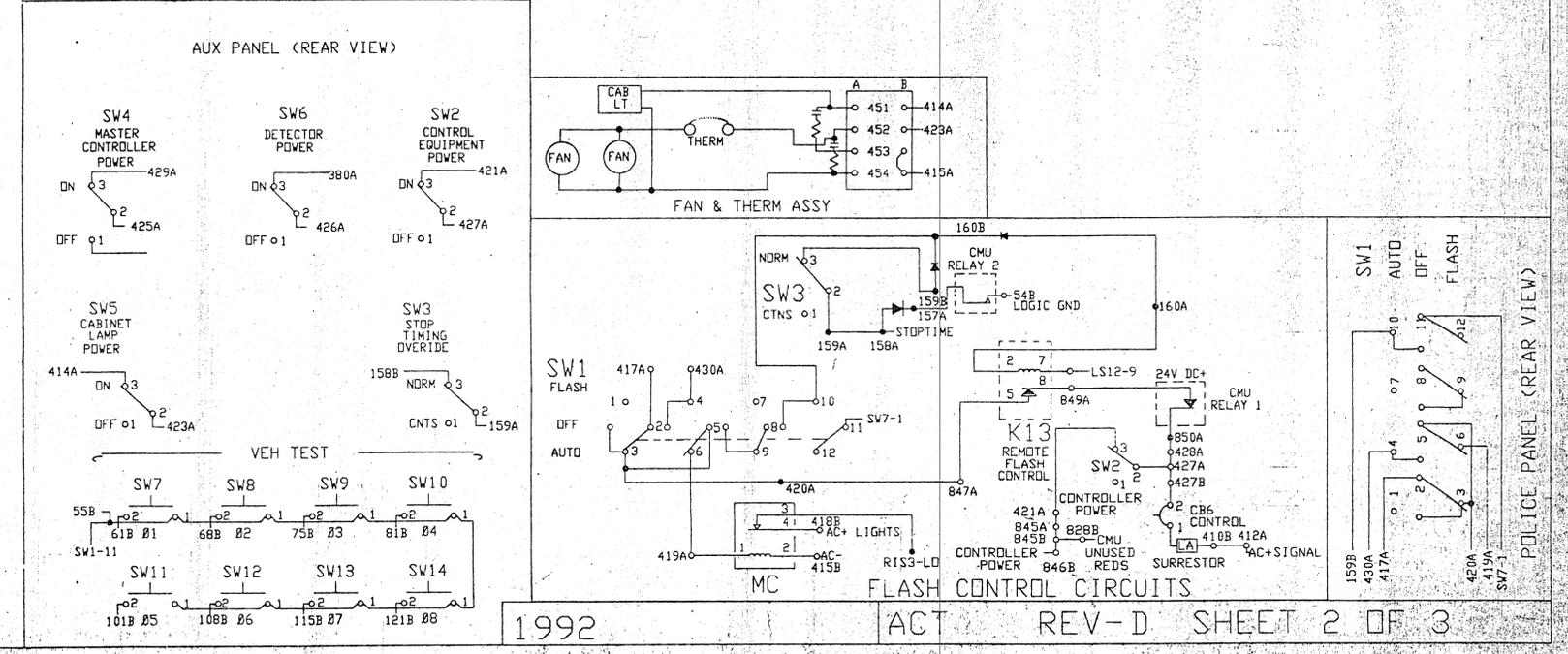
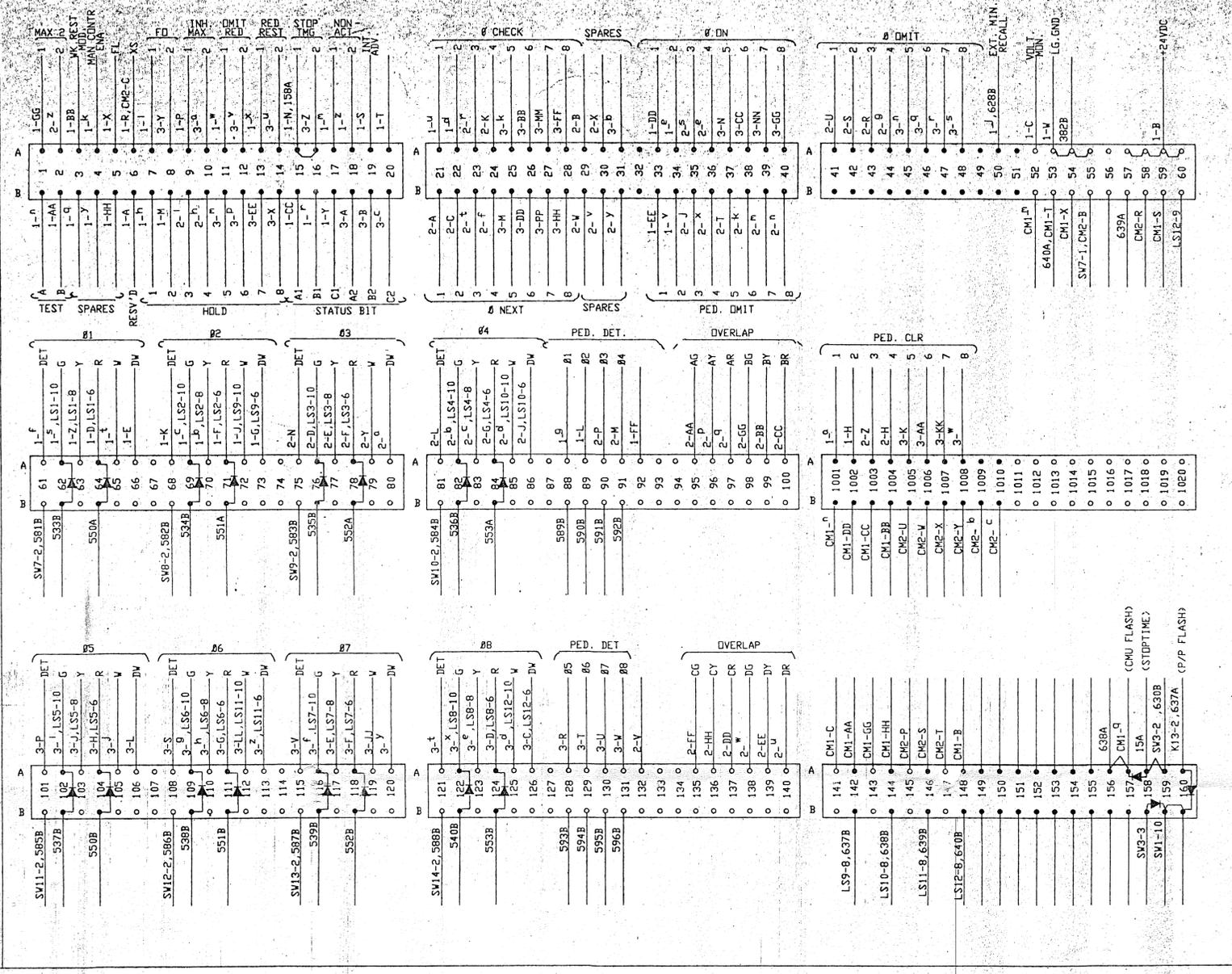
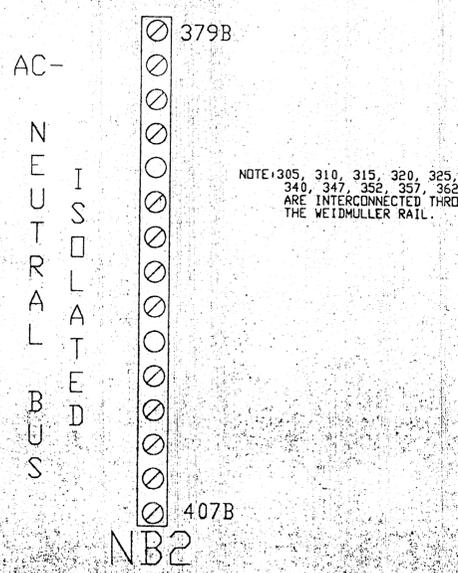
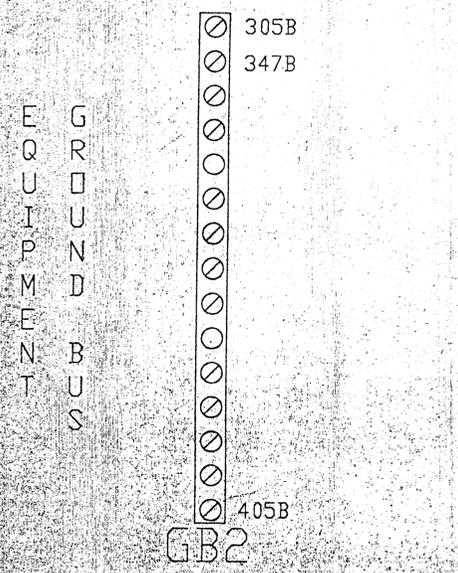
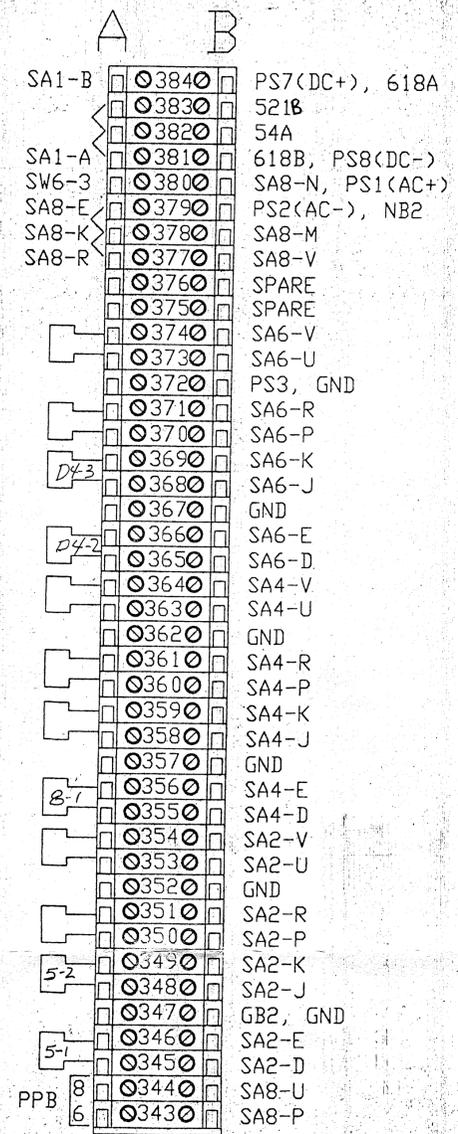
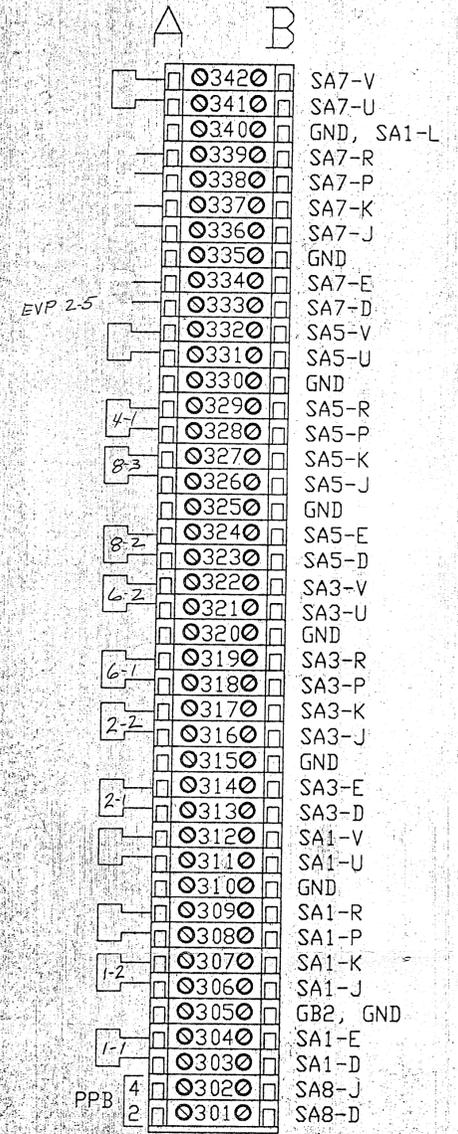


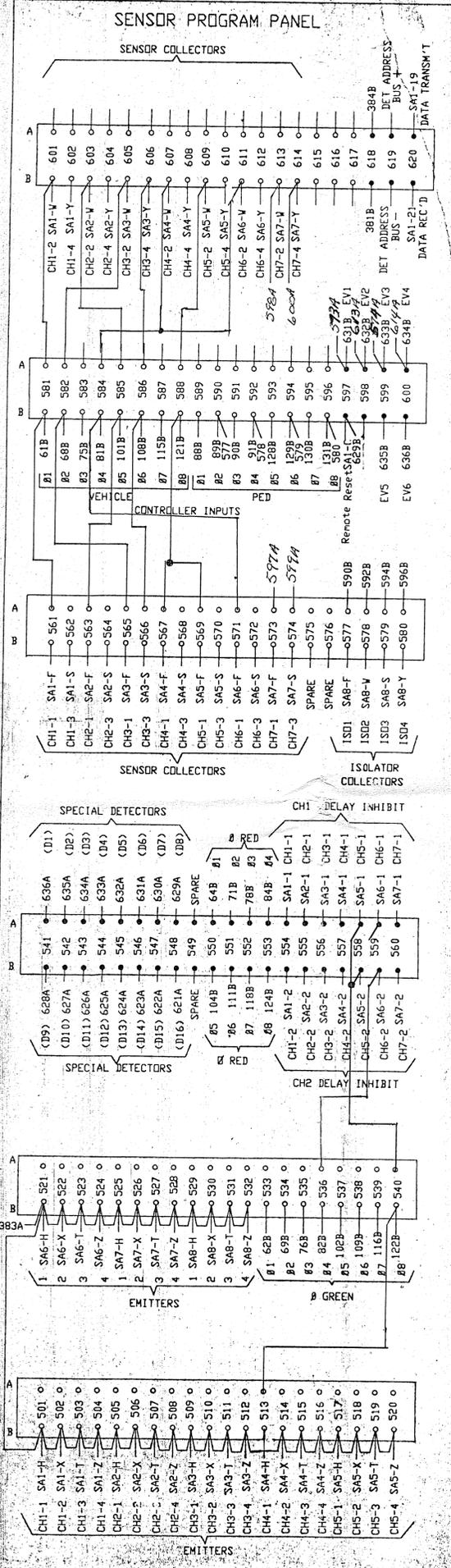
CONTROLLER INTERFACE PANEL

1	SH1 SHELL GROUND	GB3	2	SH1 SHELL GROUND	GB3	3	SH1 SHELL GROUND	GB3
	A1 RESV	6B		A1 #1 PHASE NEXT	21B		A1 STATUS BIT A2	18B
	B1 24VDC	59A		B SPARE 1	29A		B1 STATUS BIT B2	19B
	C VOLTAGE MONITOR	52A		C #2 PHASE NEXT	22B		C #8 DWK	126A
	D #1 RED	64A		D #3 GRN	76A		D #8 RED	124A
	E #1 DWK	66A		E #3 YEL	77A		E #7 YEL	117A
	F #2 RED	71A		F #3 RED	78A		F #6 RED	118A
	G #2 DWK	73A		G #4 RED	84A		G #6 RED	111A
	H #2 PCL	1002A		H #4 PCL	1004A		H #5 RED	104A
	J #2 WK	72A		J #4 DWK	86A		J #5 YEL	103A
	K #2 VEH DET	68A		K #4 CHECK	24A		K #5 PCL	1005A
	L #2 PED DET	89A		L #4 VEH DET	81A		L #5 DWK	106A
	M #2 HOLD	89A		M #4 PED DET	91A		M #5 PHASE NEXT	25B
	N STOP TIMING 1	8B		N #3 VEH DET	75A		N #5 PHASE DN	37A
	P INHIBIT MAX TERM 1	15A		P #3 PED DET	90A		P #5 VEH DET	101A
	R EXTERNAL START	9A		R #3 PHASE OMIT	43A		R #5 PED DET	108A
	S INTERVAL ADVANCE	6A		S #2 PHASE OMIT	42A		S #6 VEH DET	128A
	T INDICATOR LAMP CNT	19A		T #5 PED OMIT	37B		T #6 PED DET	108A
	U AC COMMON	20A		U #1 PHASE OMIT	37B		U #7 VEH DET	129A
	V CHASSIS GROUND	848A		V PED RECYCLE 2	41A		V #7 VEH DET	130A
	W LOGIC GROUND	GB3		W SPARE 2	132A		W #8 PED DET	115A
	X FLASH LOGIC OUT	53A		X SPARE 3	29B		X #8 HOLD	131A
	Y STATUS BIT C1	17B		Y #3 WK	30A		Y FORCE OFF 2	14B
	Z #1 YEL	63A		Z #3 PCL	1003A		Z STOP TIME 2	16A
	a #1 PCL	1001A		a #3 DWK	80A		a INHIBIT MAX TERM 2	10A
	b #2 YEL	70A		b #4 GRN	82A		b SPARE 1	31A
	c #2 GRN	69A		c #4 YEL	83A		c STATUS BIT C2	20B
	d #2 CHECK	22A		d #4 WALK	85A		d #8 WK	125A
	e #2 PHASE DN	34A		e #4 PHASE DN	36A		e #8 YEL	123A
	f #1 VEH DET	61A		f #4 PHASE NEXT	24B		f #7 GRN	116A
	g #1 PED DET	88A		g #4 PHASE OMIT	44A		g #6 GRN	109A
	h #1 HOLD	7B		h #4 HOLD	10A		h #6 YEL	110A
	i FORCE OFF 1	7A		i #3 HOLD	10B		i #5 GRN	102A
	j EXT MIN RECALL ALL 0	70A		j #3 PED OMIT	9B		j #5 WK	102A
	k MAN. CONTROL ENABLE	50A		k #6 PED OMIT	35B		k #5 CHECK	105A
	l CALL TO NON-ACT 1	4A		l #7 PED OMIT	38B		l #5 HOLD	29A
	m TEST INPUT A	1B		m #8 PED OMIT	39B		m #5 PHASE OMIT	11B
	n AC+ CONTROL	846B		n DL A YEL	12B		p #6 HOLD	45A
	o SPARE 1	3B		q #4 A RED	97A		q #6 PHASE OMIT	46A
	p STATUS BIT B1	16B		r #3 CHECK	23A		r #7 PHASE OMIT	47A
	s #1 GRN	62A		s #3 PHASE DN	35A		s #8 PHASE OMIT	48A
	t #1 WK	65A		t #3 PHASE NEXT	23B		t #8 VEH DET	121A
	u #1 CHECK	21A		u DL D RED	140A		u RED REST MODE 2	14A
	v #2 PED OMIT	34B		v SPARE 4	30B		v OMIT RED CLR 2	12A
	w OMIT RED CLR	11A		w DL D GRN	138A		w #8 PCL	1008A
	x RED REST MODE 1	13A		x #8 GRN	122A		x #8 GRN	122A
	y SPARE 2	4B		y #7 DWK	120A		y #7 DWK	120A
	z CALL TO NON-ACT II	18A		z #6 DWK	113A		z #6 DWK	113A
	AA TEST INPUT B	2B		z #6 PCL	1006A		z #6 PCL	1006A
	BB WALK REST MODIFIER	3A		BB #6 CHECK	26A		BB #6 CHECK	26A
	CC STATUS BIT A1	15B		CC DL B RED	99A		CC #6 PHASE DN	38A
	DD #1 PHASE DN	33A		DD DL C RED	100A		DD #6 PHASE DN	38A
	EE #1 PED OMIT	33B		EE DL D YEL	137A		EE #7 HOLD	26B
	FF PED RECYCLE 1	92A		FF DL C GRN	135A		FF #8 CHECK	13B
	GG MAX 2 SELECT	1A		GG DL B GRN	98A		GG #8 PHASE DN	28A
	HH SPARE 3	5B		HH DL C YEL	136A		GG #8 PHASE NEXT	28B
							JJ #7 WK	119A
							KK #7 PCL	1007A
							LL #6 WK	112A
							MM #7 CHECK	27A
							NN #7 PHASE DN	39A
							PP #7 PHASE NEXT	27B



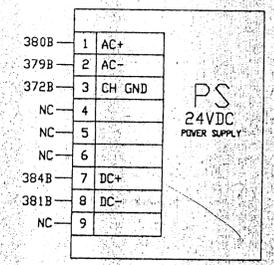
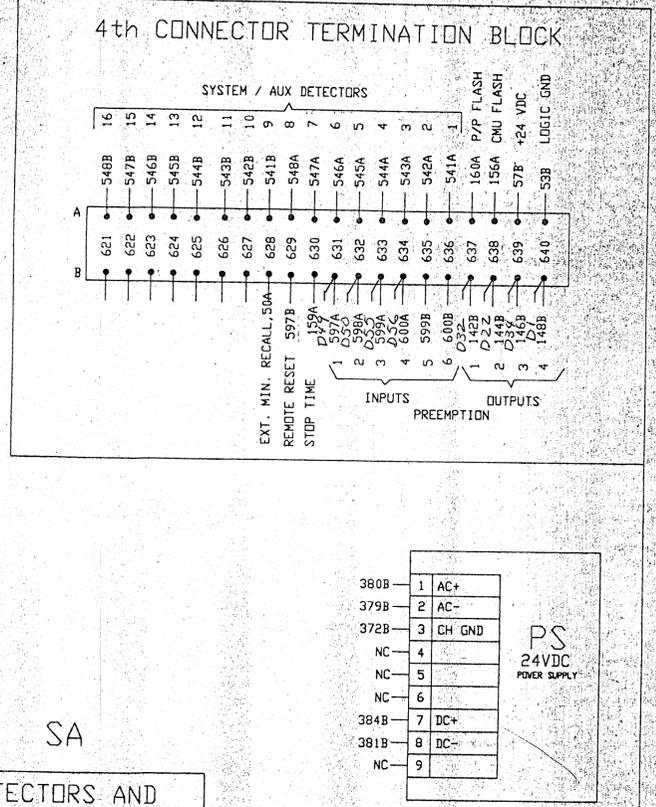


NOTE: 305, 310, 315, 320, 325, 330, 335, 340, 347, 352, 357, 362 AND 372 ARE INTERCONNECTED THROUGH THE WEIDMULLER RAIL.



SPECIAL FUNCTION CONNECTOR PIN ASSIGNMENT

PIN	FUNCTION	I/O
1	Emergency Preempt 4 Out	640B
2	Offset 3 Out	
3	Offset 4 In (Add Bit 3)	
4	SW LAMP	
5	Spare	
6	Dial 4 In	
7	Dial 6 In	
8	Special Function 2 Out	
9	Split 3 In	
10	Offset 2 In (Add Bit 1)	
11	Offset 1 In (Add Bit 0)	
12	System Detector 8	
13	Special Function 3 Out	
14	Split 2 In	
15	System Detector 1 (Seq #1)	
16	System Detector 4 (Seq #4)	
17	System Enable	
18	Dimming On	
19	Emergency Preempt 2 Out	688B
20	Railroad Preempt Out	
21	Spare	
22	Dial 2 In (Special Function 2)	
23	Coordination On (Special Function 1)	
24	Coordination Out	
25	Special Function 1 Out	
26	Dial 4 Out	
27	System Detector 5 In	
28	System Detector 3 (Seq #3)	637B
29	Emergency Preempt 1 Out	
30	Offset 1 Out	
31	Emergency Preempt 3 Out	698B
32	Dial 3 In (Special Function 3)	
33	Offset 3 In (Add Bit 2)	
34	Flash Status In	
35	Offset 5 In (Add Bit 4)	
36	System Detector 6 In	
37	System Detector 7 In	
38	Offset 4 Out	
39	Offset 2 Out	
40	Dial 5 Out	
41	System Detector 2 (Seq #2)	640A
42	Logic Ground	631B
43	Emergency Preempt 1 In	632B
44	Emergency Preempt 2 In	
45	Dial 5 Out	
46	Dial 4 Out	
47	Logic Ground	
48	Emergency Preempt 3 In	633B
49	Emergency Preempt 4 In	634B
50	Railroad Preempt In	
51	Conflict Status In	
52	Flash Command In	
53	Reserved	
54	Reserved	
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222	Reserved	



DETECTORS AND PPB ISOLATION

P/N	RACK HARNESS SA-PIN	SA1		SA2		SA3		SA4		SA5		SA6		SA7		SA8		FUNCTIONS		
		PHASE	FUNC	PHASE	FUNC	PHASE	FUNC	PHASE	FUNC	PHASE	FUNC	PHASE	FUNC	PHASE	FUNC	PHASE	FUNC			
1	A	DC GROUND	381A															NC	SPARE	A
2	B	24V DC+	384A															NC	SPARE	B
3	C	REMOTE RESET	597B															NC	SPARE	C
4	D-4	CH 1 LOOP	303B	345B	313B	355B	323B	365B	333B	301B	379A	INPUT CH 1								D
5	E-5	CH 1 LOOP	304B	346B	314B	356B	324B	366B	334B	379A	INPUT COMMON									E
6	F	ADDRESS BIT #0	619B	SA1-15	SA2-10	SA3-15	SA4-15	SA5-10	SA6-15	NC										F
7	G	CH 1 OUTPUT (+)	561B	563B	565B	567B	569B	571B	573B	577B	579B	OUTPUT CH 1 (+)								G
8	H	CH 1 OUTPUT (-)	501B	505B	509B	513B	517B	521B	525B	529B	OUTPUT CH 1 (-)									H
9	J	CH 2 LOOP	306B	348B	316B	358B	326B	368B	336B	302B	378A	INPUT CH 2								J
10	K-9	CH 2 LOOP	307B	349B	317B	359B	327B	369B	337B	378A	INPUT COMMON									K
11	L	ADDRESS BIT #1	619A	SA1-6	SA3-6	SA4-6	SA4-10	SA5-15	SA7-6	NC										L
12	M	CHASSIS GROUND	340B																	M
13	N	SPARE	NC	SA7/SA8	SA6/SA8	SA7/378B	SA6/SA8	SA7/380B	115V AC+					N						
14	P-13	LOOP CH 3	308B	350B	318B	360B	328B	370B	338B	343B	INPUT CH 3									P
15	R-14	LOOP CH 3	309B	351B	319B	361B	329B	371B	339B	377A	INPUT COMMON									R
16	S	ADDRESS BIT #2	SA1-10	SA2-6	SA2-15	SA3-10	SA5-6	SA6-10	SA7-10	NC										S
17	T	CH 3 OUTPUT (+)	562B	564B	566B	568B	570B	572B	574B	579B	OUTPUT CH 3 (+)									T
18	U-17	CH 3 OUTPUT (-)	503B	507B	511B	515B	519B	523B	527B	531B	OUTPUT CH 3 (-)									U
19	V-18	CH 4 LOOP	311B	353B	321B	363B	331B	373B	341B	344B	INPUT CH 4									V
20	W	CH 4 LOOP	312B	354B	322B	364B	332B	374B	342B	377B	INPUT COMMON									W
21	X	CH 2 OUTPUT (+)	601B	603B	605B	607B	609B	611B	613B	578B	OUTPUT CH 2 (+)									X
22	Y	CH 2 OUTPUT (-)	502B	506B	510B	514B	518B	522B	526B	530B	OUTPUT CH 2 (-)									Y
23	Z	CH 4 OUTPUT (+)	602B	604B	606B	608B	610B	612B	614B	580B	OUTPUT CH 4 (+)									Z
24	1	CH 1 GREEN	554A	555A	556A	557A	558A	559A	560A		SPARE									1
25	2	CH 2 GREEN	554B	555B	556B	557B	558B	559B	560B		SPARE									2
26	21	DATA RECEIVE	620B																	21