



# CONTROLLER INTERFACE PANEL

1	2	3
SH SHELL GROUND 1-V	AL Ø1 PHASE NEXT 21B	AL STATUS BIT A2 18B
A1 RESV. 6B	BL SPARE 1 29A	BL STATUS BIT B2 19B
B1 24VDC+ 59A	CL Ø2 PHASE NEXT 22B	CL Ø8 DWK 126A
C1 VOLTAGE MONITOR 52A	DL Ø3 GRN 76A	DL Ø9 RED 124A
D1 Ø1 RED 64A	EL Ø3 YEL 77A	EL Ø7 YEL 117A
E1 Ø1 DWK 66A	FL Ø3 RED 78A	FL Ø7 RED 118A
F1 Ø2 RED 71A	GL Ø4 RED 84A	GL Ø6 RED 111A
G1 Ø2 DWK 73A	HL Ø4 PCL 1004A	HL Ø5 RED 104A
H1 Ø2 PCL 1002A	JL Ø4 DWK 86A	JL Ø5 YEL 103A
I1 Ø2 WK 72A	KL Ø4 CHECK 24A	KL Ø5 PCL 1005A
K1 Ø2 VEH DET 68A	LL Ø4 VEH DET 81A	L Ø5 DWK 106A
L1 Ø2 PED DET 89A	ML Ø4 PED DET 91A	M Ø5 PHASE NEXT 25B
M1 Ø2 HOLD 8B	NL Ø3 VEH DET 75A	N Ø5 PHASE ON 37A
NI STOP TIMING 1 15A	PL Ø3 PED DET 90A	P Ø5 VEH DET 101A
PI INHIBIT MAX TERM 1 9A	RL Ø3 PHASE OMIT 43A	R Ø5 PED DET 128A
RI EXTERNAL START 6A	SL Ø2 PHASE OMIT 42A	S Ø6 VEH DET 109A
SI INTERVAL ADVANCE 19A	TL Ø5 PED OMIT 37B	T Ø6 PED DET 129A
TI INDICATOR LAMP CNT 20A	UL Ø1 PHASE OMIT 41A	UL Ø7 PED DET 130A
UI AC- NEUTRAL NB1	V1 PED RECYCLE 2 132A	V Ø7 VEH DET 115A
V1 CHASSIS GROUND GB1	W1 SPARE 2 29B	W Ø8 PED DET 131A
W1 LOGIC GROUND 53A	XL SPARE 3 30A	XL Ø8 HOLD 14B
X1 FLASH LOGIC OUT 5A	YL Ø3 WK 79A	YL FORCE OFF 2 14B
Y1 STATUS BIT C1 17B	ZL Ø3 PCL 1003A	ZL STOP TIME 2 8A
Z1 Ø1 YEL 63A	a1 Ø3 DWK 80A	a1 INHIBIT MAX TERM 2 16A
a1 Ø1 PCL 1001A	b1 Ø4 GRN 82A	b1 SPARE 1 10A
b1 Ø2 YEL 70A	c1 Ø4 YEL 83A	c1 STATUS BIT C2 20B
c1 Ø2 GRN 69A	d1 Ø4 WALK 85A	d1 Ø8 WK 125A
d1 Ø2 CHECK 22A	e1 Ø4 PHASE ON 36A	e1 Ø7 GRN 123A
e1 Ø2 PHASE ON 34A	f1 Ø4 PHASE NEXT 24B	f1 Ø7 GRN 116A
f1 Ø1 VEH DET 61A	g1 Ø4 PHASE OMIT 44A	g1 Ø6 GRN 109A
g1 Ø1 PED DET 88A	h1 Ø4 HOLD 10B	h1 Ø6 YEL 110A
h1 Ø1 HOLD 7B	i1 Ø3 HOLD 9B	i1 Ø5 GRN 102A
i1 FORCE OFF 1 7A	j1 Ø3 PED OMIT 35B	j1 Ø5 WK 105A
j1 EXT MIN RECALL ALL/Ø 50A	k1 Ø6 PED OMIT 38B	k1 Ø5 CHECK 25A
k1 MAN. CONTROL ENABLE 4A	m1 Ø7 PED OMIT 39B	m1 Ø5 HOLD 11B
m1 CALL TO NON-ACT I 17A	n1 Ø8 PED OMIT 40B	n1 Ø5 PHASE OMIT 45A
n1 TEST INPUT A 1B	p1 ØL A YEL 96A	p1 Ø6 HOLD 12B
p1 AC- CONTROL 846B	q1 ØL A RED 97A	q1 Ø6 PHASE OMIT 46A
q1 SPARE 1 3B	r1 Ø3 CHECK 23A	r1 Ø7 PHASE OMIT 47A
r1 STATUS BIT B1 16B	s1 Ø3 PHASE ON 35A	s1 Ø8 PHASE OMIT 48A
s1 Ø1 GRN 62A	t1 Ø3 PHASE NEXT 23B	t1 Ø8 VEH DET 121A
t1 Ø1 WK 65A	u1 ØL D RED 23B	u1 RED REST MODE 2 14A
u1 Ø1 CHECK 21A	v1 Ø2 PED OMIT 34B	v1 OMIT RED CLR 2 14A
v1 Ø2 PED OMIT 21A	w1 ØL D GRN 30B	w1 Ø8 PCL 12A
w1 OMIT RED CLR 34B	x1 Ø8 GRN 130A	x1 Ø8 GRN 1008A
x1 RED REST MODE 1 11A	y1 SPARE 5 36B	y1 Ø7 DWK 122A
y1 SPARE 2 13A	z1 MAX 2 SELECT 2 31B	z1 Ø6 DWK 120A
z1 CALL TO NON-ACT II 4B	AA1 ØL A GRN 95A	AA1 Ø6 PCL 113A
AA1 TEST INPUT B 2B	BB1 ØL B YEL 99A	BB1 Ø6 CHECK 1006A
BB1 WALK REST MODIFIER 3A	CC1 ØL B RED 100A	CC1 Ø6 PHASE ON 26A
CC1 STATUS BIT A1 15B	DD1 ØL C RED 137A	DD1 Ø6 PHASE NEXT 38A
DD1 Ø1 PHASE ON 33A	EE1 ØL D YEL 139A	EE1 Ø7 HOLD 26B
EE1 Ø1 PED OMIT 33B	FF1 Ø8 CHECK 28A	FF1 Ø7 HOLD 13B
FF1 PED RECYCLE 1 92A	GG1 Ø8 PHASE ON 40A	GG1 Ø8 CHECK 28A
GG1 MAX 2 SELECT 1A	HH1 Ø8 PHASE NEXT 28B	HH1 Ø8 PHASE ON 40A
HH1 SPARE 3 5B	JJ1 Ø7 WK 119A	HH1 Ø8 PHASE NEXT 28B
	KK1 Ø7 PCL 1007A	JJ1 Ø7 WK 119A
	LL1 Ø6 WK 112A	KK1 Ø7 PCL 1007A
	MM1 Ø7 CHECK 27A	LL1 Ø6 WK 112A
	NN1 Ø7 PHASE ON 39A	MM1 Ø7 CHECK 27A
	PP1 Ø7 PHASE NEXT 27B	NN1 Ø7 PHASE ON 39A
		PP1 Ø7 PHASE NEXT 27B



